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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OKAJIMA, Yoshinori**

Group Art Unit: **2816**

Serial No.: **10/708,145**

Examiner: **Cassandra F. COX**

Filed: **February 11, 2004**

P.T.O. Confirmation No.: **2144**

**FOR: TIMING CONTROLLER AND CONTROLLED DELAY
CIRCUIT FOR CONTROLLING TIMING OR DELAY
TIME OF A SIGNAL BY CHANGING PHASE THEREOF**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(c)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 27, 2006

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. A copy of the non-U.S. patent document is attached.

This Information Disclosure Statement is being submitted after issuance of a first official action on the merits and expiration of the three month period following the filing date or the entry of the national stage for the above-captioned application, but prior to issuance of either a final official action or a Notice of Allowance. A check is attached hereto which covers the \$ 180.00 fee set forth in 37 CFR 1.17(p).

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. See 37 CFR 1.104(a) concerning the PTO duty to consider and use any such information. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached

Information Disclosure Statement Under 37 CFR 1.97(c)
U.S. Patent Application Serial No. 10/708,145

Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is hereby authorized to charge any additional fee (or credit any overpayment) associated with this statement to our Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449

Copy of Japanese Office Action dated January 10, 2006

Reference (1)

Check for \$ 180.00



INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 960637D	Serial No. 10/708,145
	Applicant(s): OKAJIMA, Yoshinori	
	Filing Date: February 11, 2004	Group Art Unit: 2816

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,245,231	Kocis et al.	09-14-1993	307	606	12-30-1991
_____	AB						
_____	AC						
_____	AD						
_____	AE						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AF				
_____	AG				
_____	AH				
_____	AI				
_____	AJ				

OTHER DOCUMENTS

_____	AK	Copy of Japanese Office Action dated January 10, 2006, 2 pages.
_____	AL	Timo Rahkonen et al.: "The Use of Stabilized CMOS Delay Lines in the Digitization of Short Time Intervals," IEEE International Symposium on June 16, 1991, vol. 4, pp. 2252-2255.
Examiner _____ Date Considered _____		